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☐ 21. Document ID: US 5065104 A

L2: Entry 21 of 24

File: USPT

Nov 12, 1991

US-PAT-NO: 5065104

DOCUMENT-IDENTIFIER: US 5065104 A

TITLE: Fault sensing with an artificial reference potential provided by an isolated capacitance effect

DATE-ISSUED: November 12, 1991

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Kusko; Alexander	Needham Heights	MA	02194	
Medora; Noshirwan K.	North Attleborough	MA		

US-CL-CURRENT: 324/508; 324/122, 324/133, 340/650, 361/42

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC
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☐ 22. Document ID: US 4900695 A

L2: Entry 22 of 24

File: USPT

Feb 13, 1990

US-PAT-NO: 4900695

DOCUMENT-IDENTIFIER: US 4900695 A

TITLE: Semiconductor integrated circuit device and process for producing the same

DATE-ISSUED: February 13, 1990

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Takahashi; Takahiko	Tokyo			JP
Itoh; Funikazu	Fujisawa			JP
Shimase; Akira	Yokohama			JP
Yamaguchi; HIroshi	Fujisawa			JP
Hongo; Mikio	Yokohama			JP
Haraichi; Satoshi	Yokohama			JP

US-CL-CURRENT: 438/625; 250/492.2, 257/750, 257/774, 257/E21.575, 257/E21.586, 257/E21.592, 257/E21.596, 257/E23.148, 438/635, 438/637, 438/641, 438/676, 438/940

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC
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☐ 23. Document ID: US 4575462 A

L2: Entry 23 of 24

File: USPT

Mar 11, 1986

US-PAT-NO: 4575462

DOCUMENT-IDENTIFIER: US 4575462 A

TITLE: Method of growing an alloy film by a layer-by-layer process on a substrate, and a method of making a semiconductor device

DATE-ISSUED: March 11, 1986

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Dobson; Peter J.	Croydon			GB2
Foxon; Charles T.	Crawley			GB2
Neave; James H.	Horsham			GB2

US-CL-CURRENT: 427/9; 117/105, 117/85, 117/86, 427/10, 438/14

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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☐ 24. Document ID: US 3896301 A

L2: Entry 24 of 24

File: USPT

Jul 22, 1975

US-PAT-NO: 3896301

DOCUMENT-IDENTIFIER: US 3896301 A

TITLE: Solid state DC times AC multiplier

DATE-ISSUED: July 22, 1975

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Resta; Vincent A.	Lauderdale Lakes	FL		

US-CL-CURRENT: 708/839; 327/306, 327/356, 701/300

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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